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Re: U.S. Utility Patent Application
Appl. No. 10/625,702; Filed: July 24, 2003
For: **Subranging Analog to Digital Converter with Multi-Phase Clock
Timing**
Inventors: VAN DER GOES *et al.*
Our Ref: 1875.2820002

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Reply Under 37 C.F.R. § 1.116;
2. List of cited references on Form PTO/SB/08A (1 sheet); and
3. One (1) return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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RES/MRM:bac
Enclosures

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